P-CHANNEL

International **ICR** Rectifier **JANTX2N6845 HEXFET® POWER MOSFET JANTXV2N6845** [REF:MIL-PRF-19500/563] [GENERIC:IRFF9120]

-100 Volt, 0.60Ω HEXFET

HEXFET technology is the key to International Rectifier's advanced line of power MOSFET transistors. The efficient geometry achieves very low onstate resistance combined with high transconductance.

HEXFET transistors also feature all of the well-establish advantages of MOSFETs, such as voltage control, very fast switching, ease of paralleling and electrical parameter temperature stability. They are well-suited for applications such as switching power supplies, motor controls, inverters, choppers, audio amplifiers, and high energy pulse circuits, and virtually any application where high reliability is required.

Product Summarv

Part Number	BVDSS	RDS(on)	ID	
JANTX2N6845	-100V	0.600	-4.0A	
JANTXV2N6845	-1000	0.60Ω	-4.0A	

Features:

- Avalanche Energy Rating
- Dynamic dv/dt Rating
- Simple Drive Requirements
- Ease of Paralleling
- Hermetically Sealed

Absolute Maximum Ratings

	Parameter	JANTX2N6845, JANTXV2N6845	Units
ID @ VGS = -10V, TC = 25°C	Continuous Drain Current	-4.0	
ID @ VGS = -10V, TC = 100°C	Continuous Drain Current	-2.6	A
IDM	Pulsed Drain Current 1	-16	
P _D @ T _C = 25°C	Max. Power Dissipation	20	W
	Linear Derating Factor	0.16	W/K (5)
VGS	Gate-to-Source Voltage	±20	V
dv/dt	Peak Diode Recovery dv/dt 3	-5.0	V/ns
Тј	Operating Junction	-55 to 150	
TSTG	Storage Temperature Range		
	Lead Temperature	300 (0.063 in. (1.6mm) from	
		case for 10.5 seconds)	℃
	Weight	0.98 (typical)	g

	Parameter	Min.	Тур.	Max.	Units	Test Conditions	
BVDSS	Drain-to-Source Breakdown Voltage	-100		_	V	VGS = 0V, ID = -1.0 mA	
$\Delta BV_{DSS}/\Delta T_{J}$	Temperature Coefficient of Breakdown Voltage	—	-0.10	_	V/°C	Reference to 25°C, $I_D = -1.0$ mA	
RDS(on)	Static Drain-to-Source	—		0.60		VGS = -10V, ID = -2.6A ⁽⁴⁾	
	On-State Resistance	—	_	0.69	Ω	VGS = -10V, ID = -4.0A	
VGS(th)	Gate Threshold Voltage	-2.0	_	-4.0	V	$V_{DS} = V_{GS}$, $I_{D} = -250 \mu A$	
gfs	Forward Transconductance	1.25	_		S (0)	VDS > -15V, IDS = -2.6A ④	
IDSS	Zero Gate Voltage Drain Current	—	-	-25	•	VDS = 0.8 x Max Rating, VGS = 0V	
		—	_	-250	μA	VDS = 0.8 x Max Rating	
						VGS = 0V, TJ = 125°C	
IGSS	Gate-to-Source Leakage Forward	—	_	-100	nA	VGS = -20V	
IGSS	Gate-to-Source Leakage Reverse	—		100		VGS = 20V	
Qg	Total Gate Charge	4.3	—	16.3		VGS = -10V, ID = -4.0A	
Qgs	Gate-to-Source Charge	1.3	-	4.7	nC	VDS = Max. Rating x 0.5	
Qgd	Gate-to-Drain ("Miller") Charge	1.0	_	9.0		see figures 6 and 13	
td(on)	Turn-On Delay Time	—		60		VDD = -50V, ID = -4.0A,	
tr	Rise Time	—	_	100	-	RG = 7.5Ω, VGS = -10V	
td(off)	Turn-Off Delay Time	—	—	50	ns		
tf	Fall Time	—	_	70		see figure 10	
LD	Internal Drain Inductance	_	5.0		nH	Measured from the drain lead, 6mm (0.25 in.) from package to center of die.	
LS	Internal Source Inductance	_	15			Measured from the source lead, 6mm (0.25 in.) from package to source bonding pad.	
C _{iss}	Input Capacitance		380			$V_{GS} = 0V, V_{DS} = -25V$	
C _{OSS}	Output Capacitance	—	170	—	pF	f = 1.0 MHz	
C _{rss}	Reverse Transfer Capacitance	_	45			see figure 5	

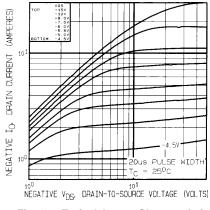
Electrical Characteristics @ Tj = 25°C (Unless Otherwise Specified)

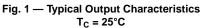
Source-Drain Diode Ratings and Characteristics

	Parameter		Min.	Тур.	Max.	Units	Test Conditions
١s	IsContinuous Source Current (Body Diode)ISMPulse Source Current (Body Diode) ①		_	_	-4.0	A	Modified MOSFET symbol showing the
ISM			_	—	-16		integral reverse p-n junction rectifier.
VSD	Diode Forward Voltage		—	—	-4.8	V	Tj = 25°C, IS = -4.0A, VGS = 0V ④
t _{rr}	Reverse Recovery Time		—	—	200	ns	Tj = 25°C, IF = -4.0A, di/dt ≤ -100A/μs
QRR	Reverse Recovery Charge		—	—	3.1	μC	V _{DD} ≤ -50V ④
ton	Forward Turn-On Time	Intrinsic turn-on time is negligible. Turn-on speed is substantially controlled by $L_{S} + L_{D}$.					

Thermal Resistance

	Parameter	Min.	Тур.	Max.	Units	Test Conditions
RthJC	Junction-to-Case		—	6.25		
R _{thJA}	Junction-to-Ambient	_	_	175	K/W	Typical socket mount





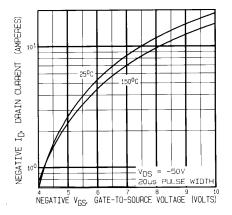


Fig. 3 — Typical Transfer Characteristics

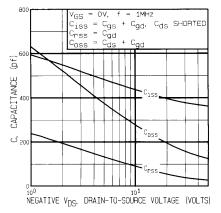


Fig. 5 — Typical Capacitance Vs. Drain-to-Source Voltage

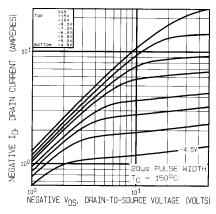


Fig. 2 — Typical Output Characteristics $T_C = 150^{\circ}C$

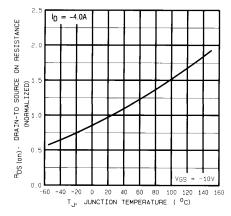


Fig. 4 — Normalized On-Resistance Vs.Temperature

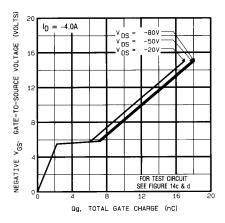
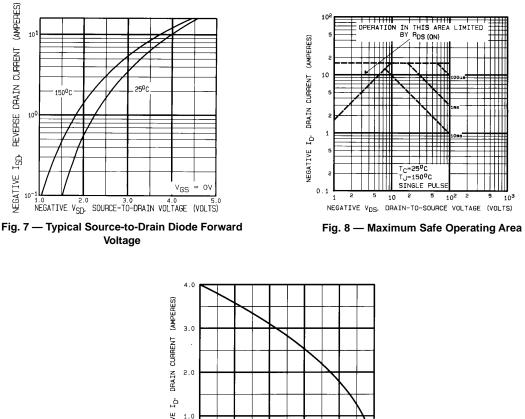


Fig. 6 — Typical Gate Charge Vs. Gate-to-Source Voltage



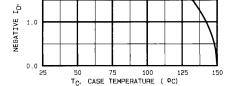


Fig. 9 — Maximum Drain Current Vs. Case Temperature

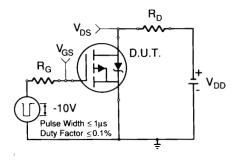
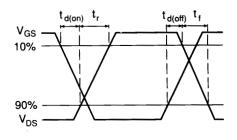


Fig. 10a — Switching Time Test Circuit





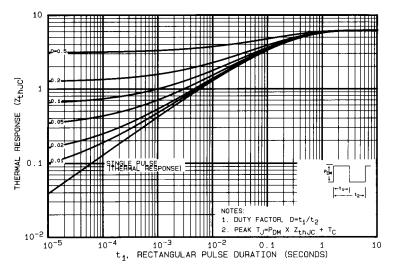


Fig. 11 — Maximum Effective Transient Thermal Impedance, Junction-to-Case Vs. Pulse Duration

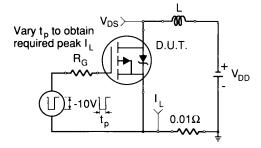


Fig. 12a — Unclamped Inductive Test Circuit

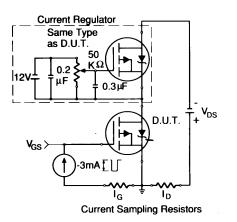


Fig. 13a — Gate Charge Test Circuit

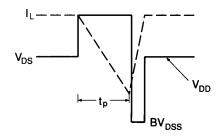
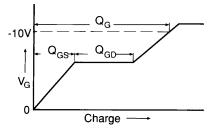


Fig. 12b — Unclamped Inductive Waveforms

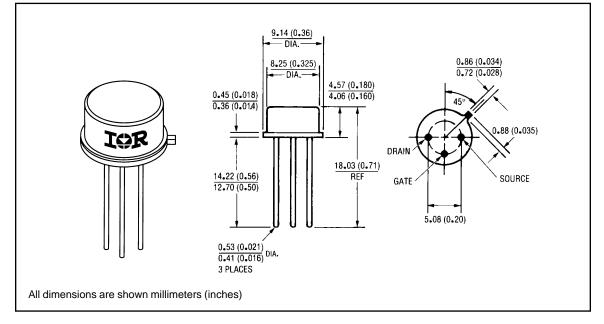




- Repetitive Rating; Pulse width limited by maximum junction temperature. (see figure 11)
- ② @ V_{DD} = -25V, Starting T_J = 25°C, EAS = $[0.5 * L * (I_L^2) * [BVDSS/(BVDSS-VDD)]$ Peak I_L = -4.0A, V_{GS} = -10V, 25 ≤ R_G ≤ 200Ω

- 3 ISD \leq -4.0A, di/dt \leq -110A/ $\!\mu s,$
- $V_{DD} \le BV_{DSS}, T_J \le 150^{\circ}C$
- (4) Pulse width \leq 300 $\mu s;$ Duty Cycle \leq 2%
- ⑤ K/W = °C/W W/K = W/°C

Case Outline and Dimensions — TO-205AF (Modified TO-39)



International

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